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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Gray et al.

Attorney Docket No.: ALTRP104/A1212

Application No.: 10/727,781

Examiner: Not yet assigned

Filed: December 3, 2003

Group: 2857

Title: METHODS AND APPARATUS FOR
GENERATING TEST INSTRUCTION
SEQUENCES



CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as first-class mail on June 2, 2005 in an envelope addressed to the Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450.

Signed: _____

A handwritten signature in black ink, appearing to read "Joyce L. Ferreira".

Joyce L. Ferreira

**INFORMATION DISCLOSURE STATEMENT
37 CFR §§1.56 AND 1.97(b)**

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached PTO Form 1449, copies of which are attached, may be material to examination of the above-identified patent application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR §§1.56 and 1.97. The Examiner is requested to make these references of official record in this application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

This Information Disclosure Statement is: (i) filed within three (3) months of the filing date of the above-referenced application, (ii) believed to be filed before the mailing date of a first Office Action on the merits, or (iii) believed to be filed before the mailing of a first Office Action after the filing of a Request for Continued Examination under §1.114. Accordingly, it is believed that no fees are due in connection with the filing of this Information Disclosure Statement. However, if it is determined that any fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 500388 (Order No. ALTRP104).

Respectfully submitted,

BEYER WEAVER & THOMAS, LLP

A handwritten signature in black ink, appearing to read "Godfrey K. Kwan".

Godfrey K. Kwan
Registration No. 46,850

P.O. Box 70250
Oakland, CA 94612-0250



Form 1449 (Modified) Information Disclosure Statement By Applicant (Use Several Sheets if Necessary)	Atty Docket No. ALTRP104/A1212	Application No.: 10/727,781
	Applicant: Gray et al. Filing Date December 3, 2003	Group 2857

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A1						
	A2						
	A3						
	A4						
	A5						

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	B1							
	B2							
	B3							
	B4							
	B5							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	C1	Tehranipour et al., "Embedded Test for Processor and Memory Cores in System-on-Chips", VLSI Circuits and Systems Laboratory, Dept of ECE, The University of Tehran," September 2003
	C2	Camphenhout et al., "High-Level Test Generation for Design Verification of Pipelined Microprocessors", ACM 1-58113-109-7/99/06, 1999
	C3	
Examiner		Date Considered

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.